

FIG. 1

Figure 1a: FUNCTIONAL BLOCK DIAGRAM OF SYSTEM 100 (includes 5 schematics)

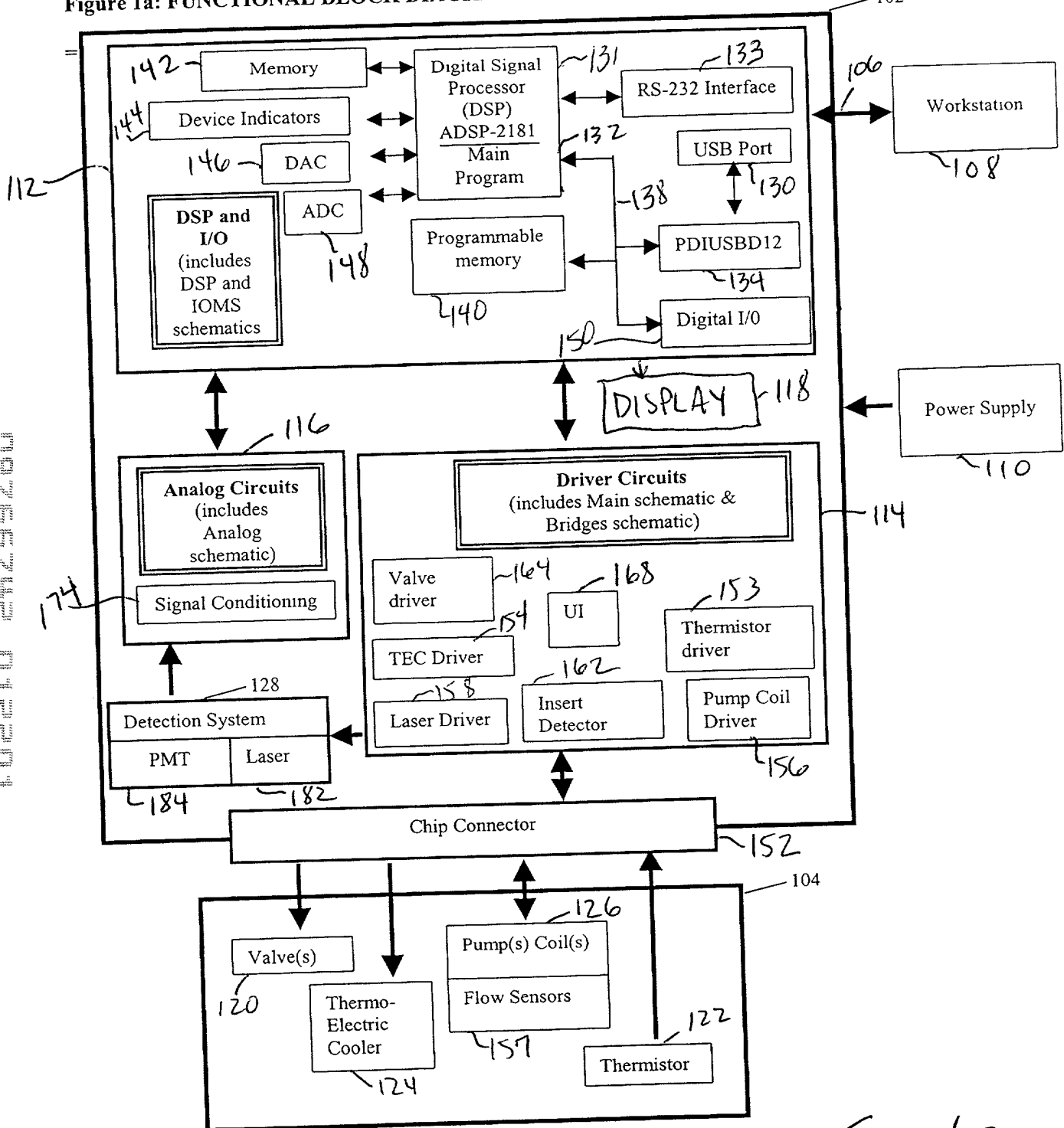
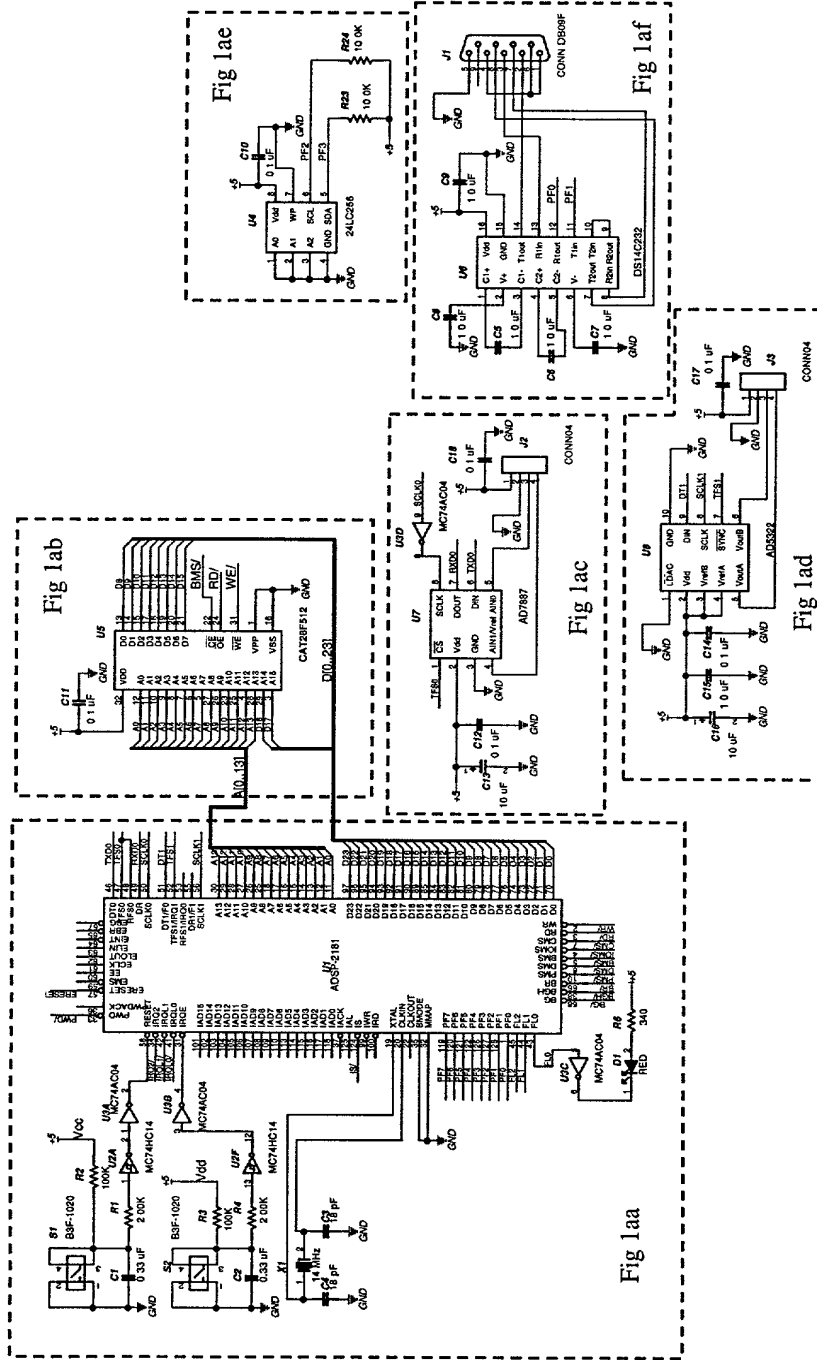
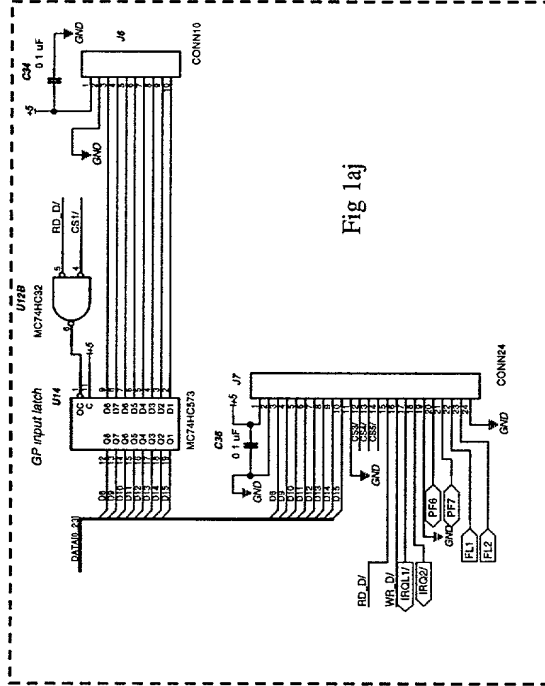
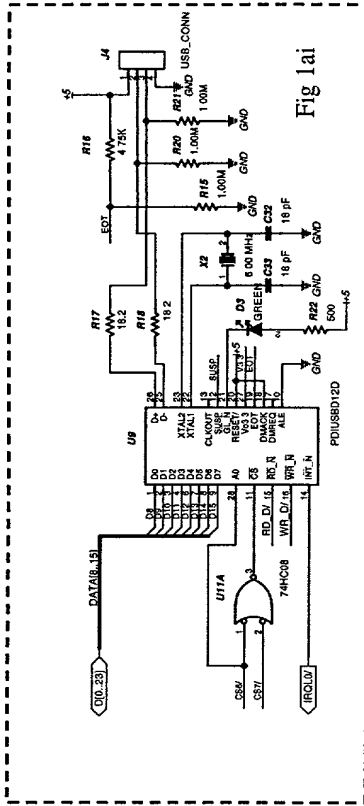
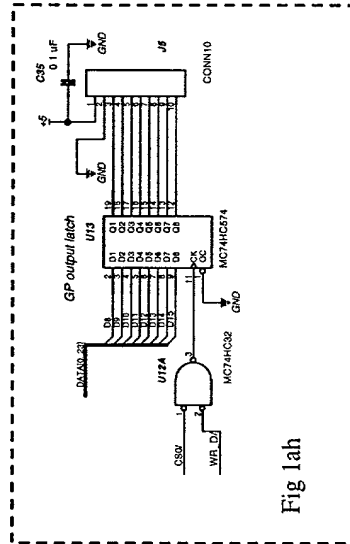
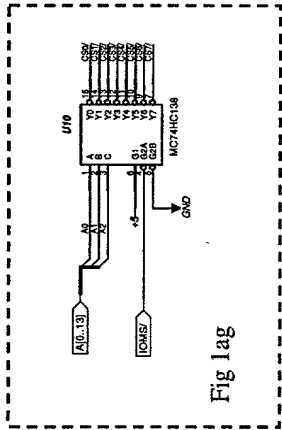


FIG. 1a



DSP Schematic



IOMS Schematic

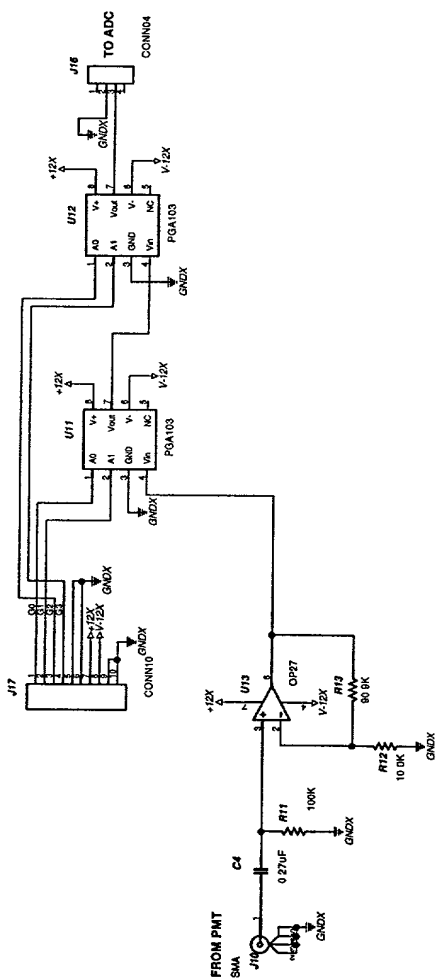


Fig 1ak

Analog Schematic

Top View

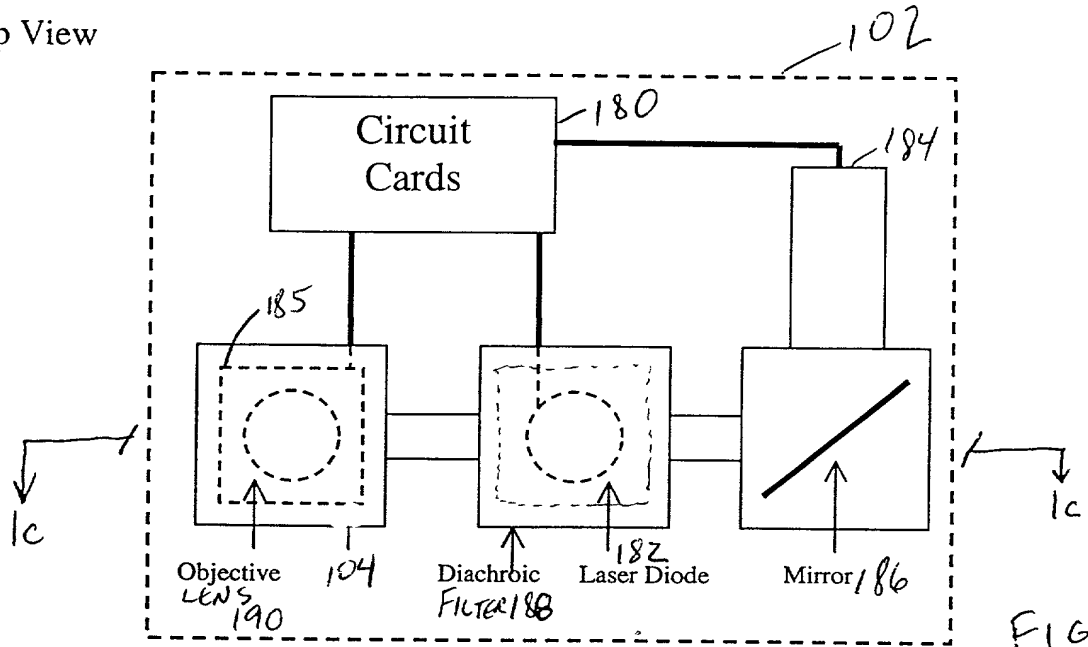


FIG. 1b

Side View Cross-Section

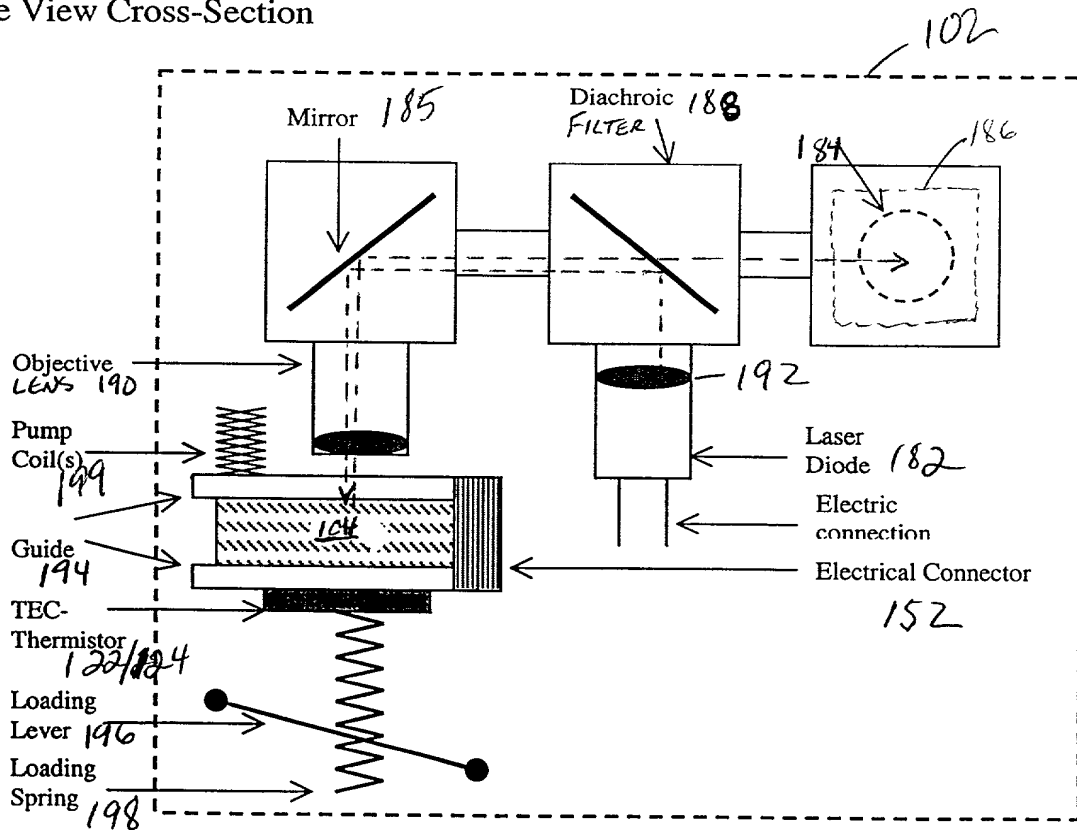


FIG. 1c



FIG. 2

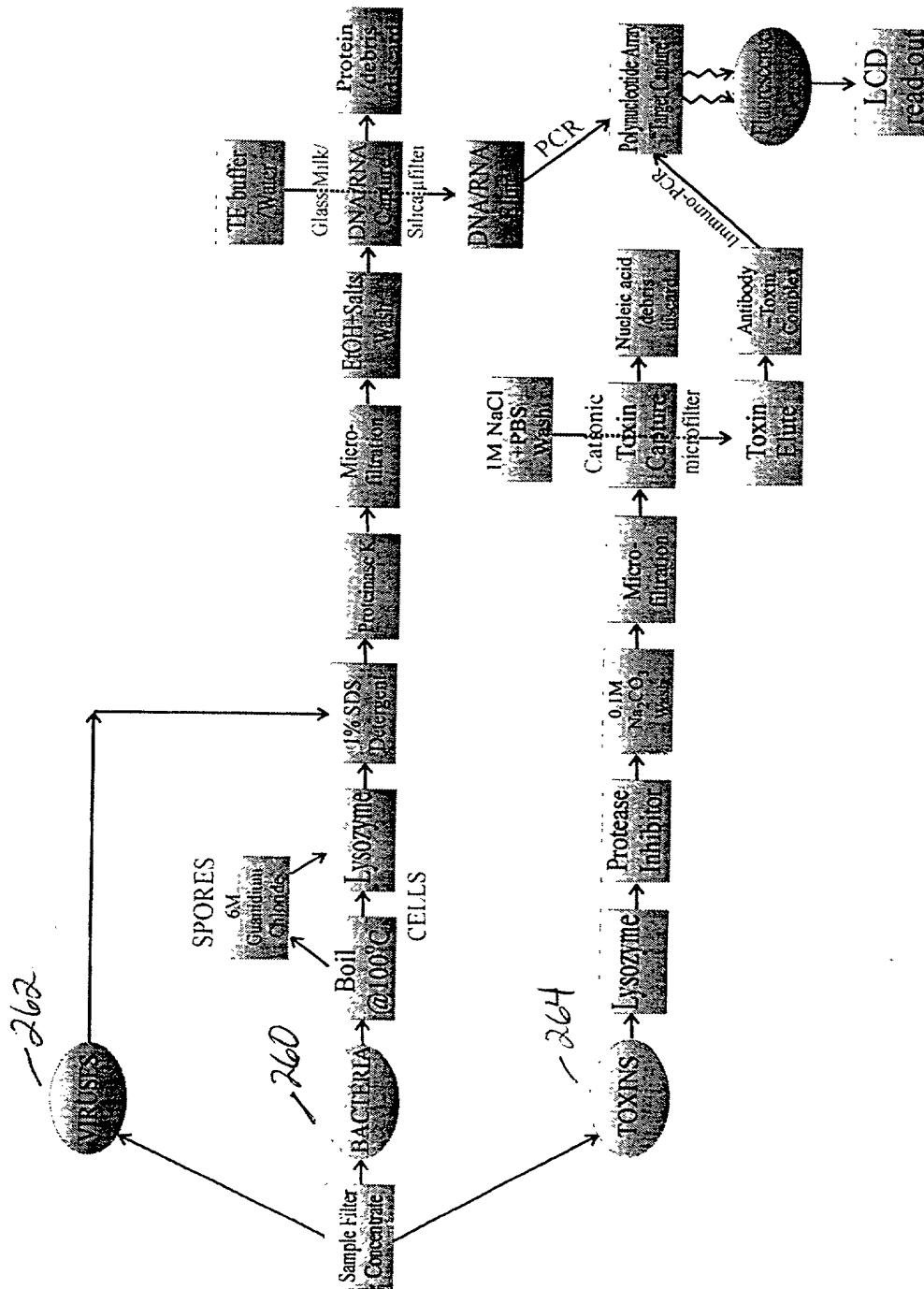


FIG. 2a

300

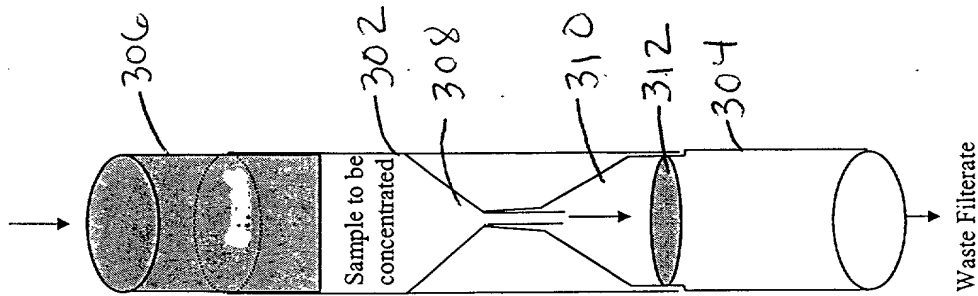


FIG. 3a

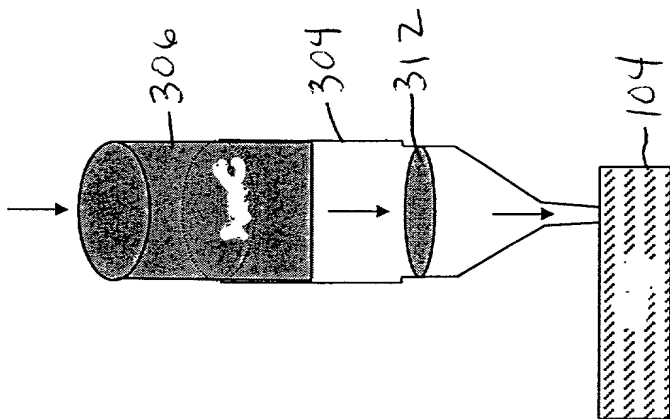


FIG. 3b

Fig. 3C

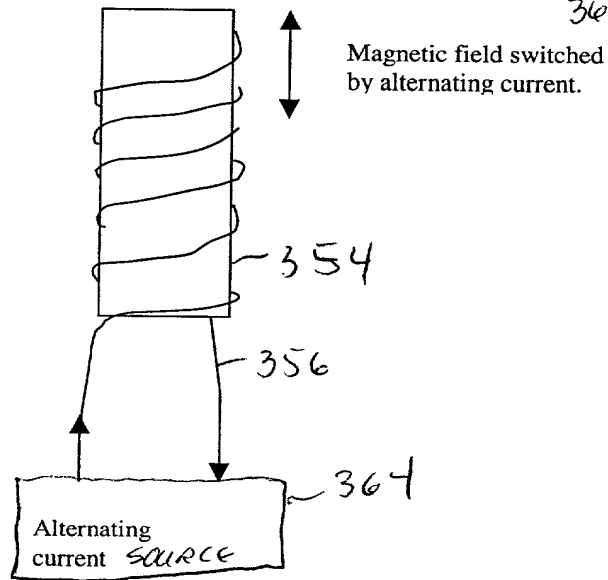
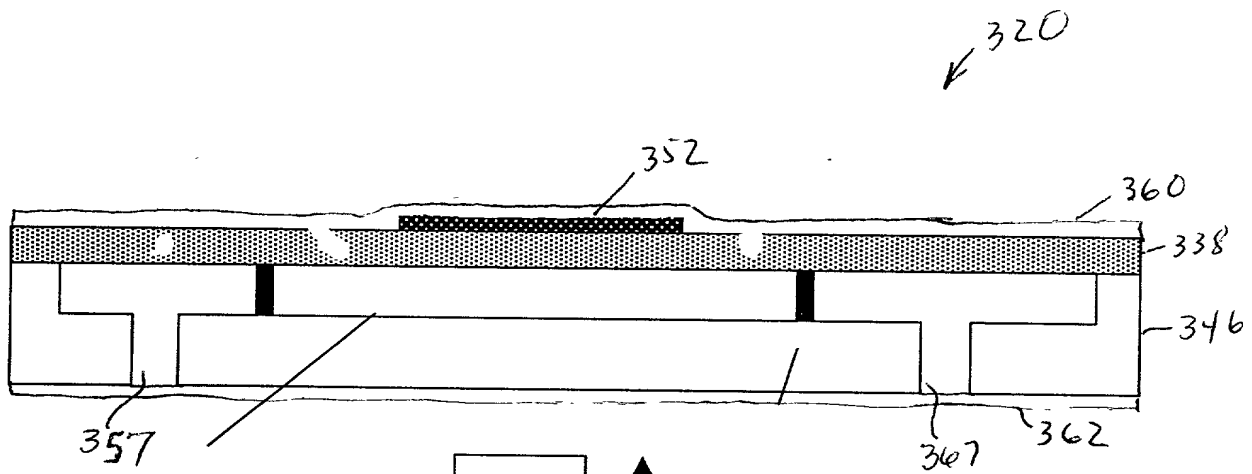
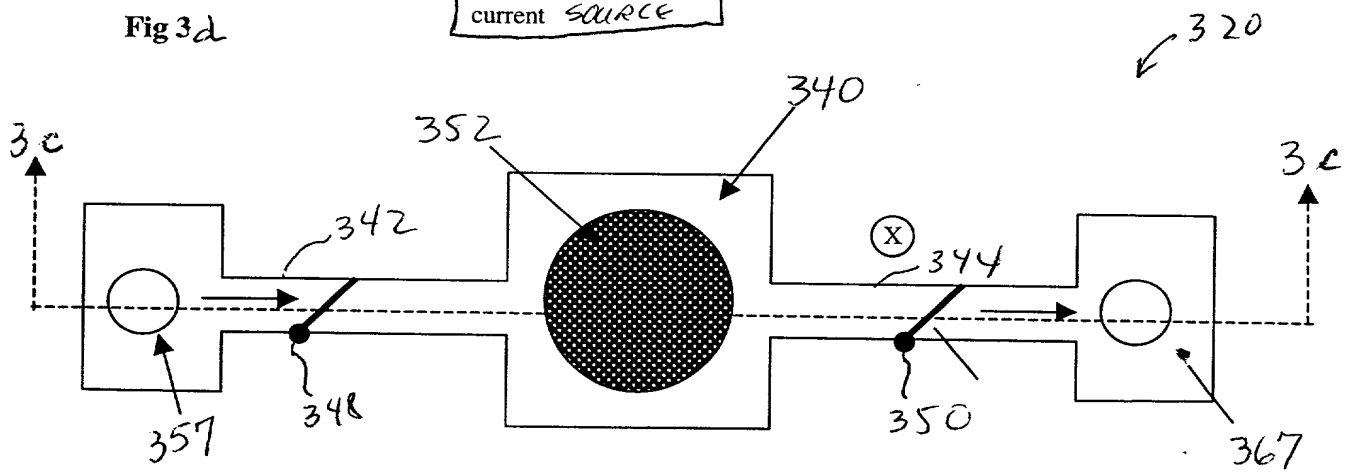


Fig 3d



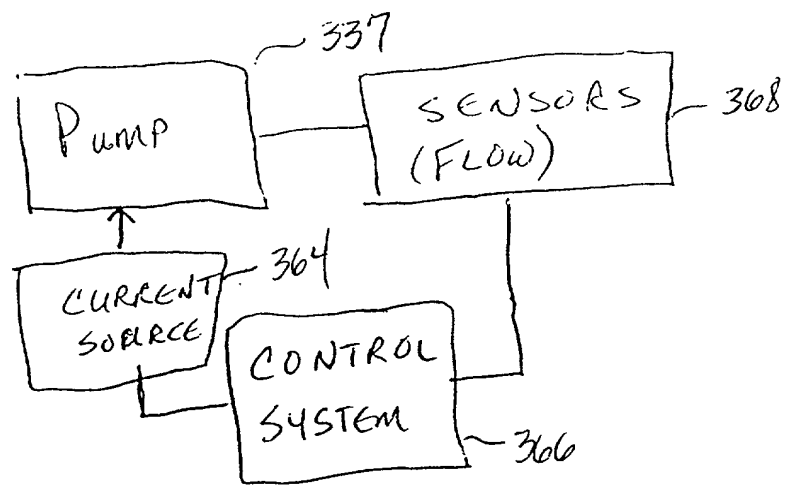
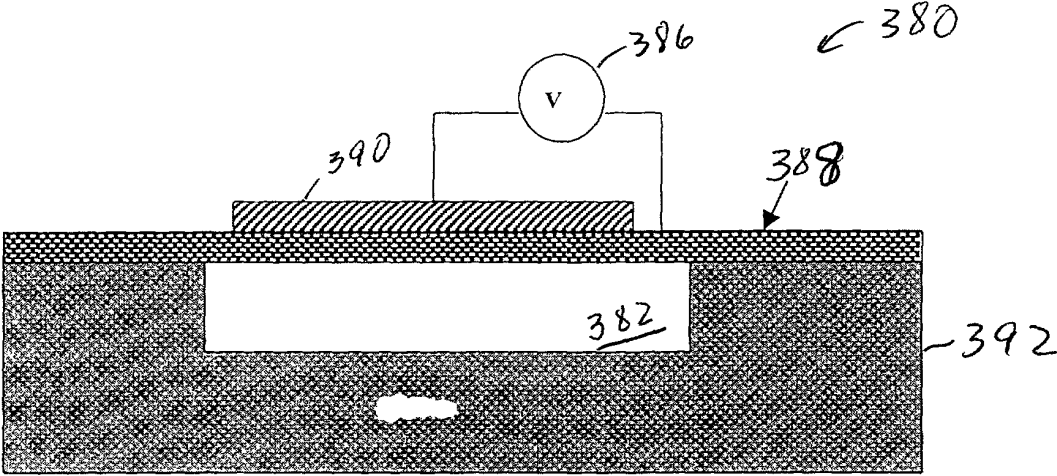


FIG. 3e

Fig 3f



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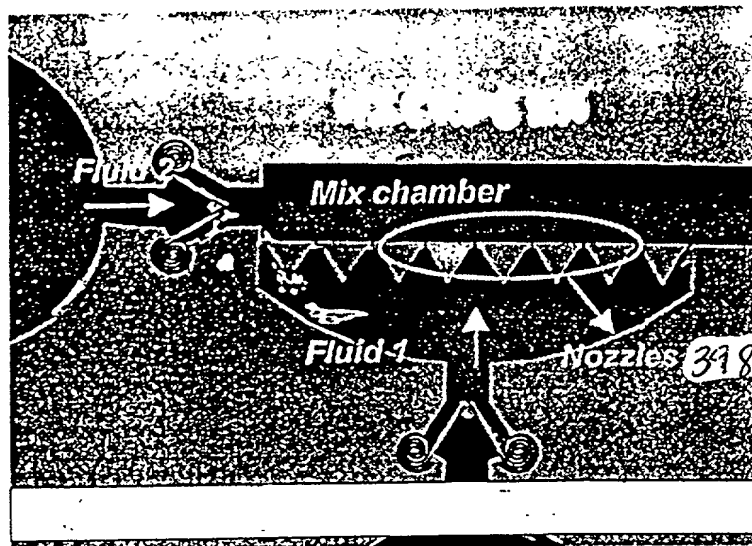


FIG. 3g

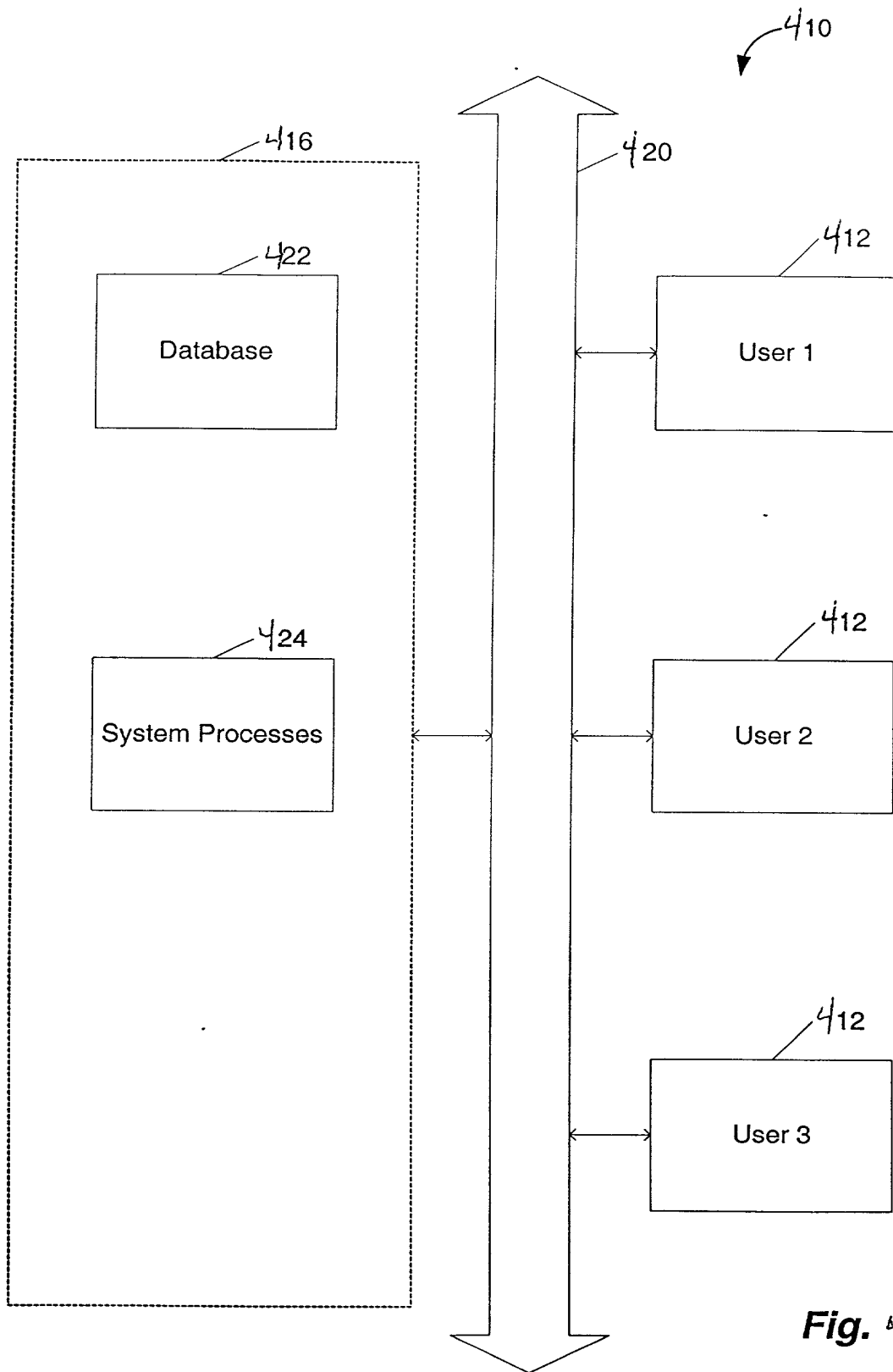


Fig. 4